

General Description

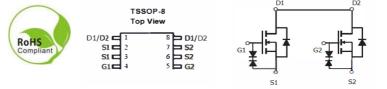
The AO8814 is the Dual N-Channel logic enhancement mode power field effect transistor which is produced using high cell density advanced trench technology to provide excellent Ros(ON>• This high density process is especially tailored to minimize on-state resistance. These devices are particularly suited for low voltage application, and low in-lin power loss are needed in a very small outline surface mount package

Features

V _{DSS}	20	V
ID	7	А
$R_{DS(ON)(atVGS}=4.5V)$	12	mΩ
$R_{DS(ON)(atVGS=2.5V)}$	15	mΩ

Application

- Power Management in Note Book
- > Portable Equipment
- Battery Powered System



Package Marking and Ordering Information

Product ID	Pack	Marking	Qty(PCS)
AO8814	TSSOP-8	8814	3000

Absolute Maximum Ratings (Tc=25°C unless otherwise noted)

Symbol	Parameter	Rating	Units
VDS	Drain-Source Voltage	20	V
Vgs	Gate-Source Voltage	±10	V
la	Drain Current – Continuous (Tc=25°C	7.0	А
ID	Drain Current – Continuous (Tc=100°C)	6.0	А
Ідм	Drain Current – Pulsed ¹	30	А
EAS	Single Pulse Avalanche Energy ²	14	mJ
IAS	Single Pulse Avalanched Current ²	1	А
PD	Power Dissipation (T _C =25 $^{\circ}$ C)	1.5	W
	Power Dissipation – Derate above 25°C	0.017	W/°C
Тѕтс	Storage Temperature Range	-55 to 150	°C
TJ	Operating Junction Temperature Range	-55 to 150	°C



Electrical Characteristics (T_J=25 °C, unless otherwise noted)

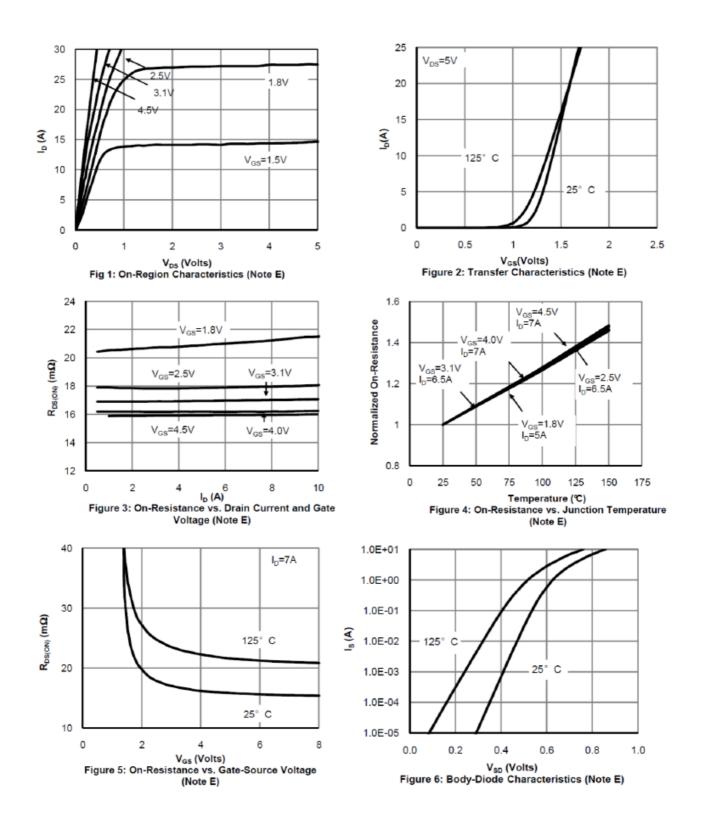
Symbol					Max		
Static Parar	neters						
V _{(BR)DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250uA	20			V	
V _{GS (th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =250uA	0.4	0.75	1.0	V	
I _{GSS}	Gate Leakage Current	V _{DS} =0V, V _{GS} =±8V			±10	uA	
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =20V, V _{GS} =0			1	uA	
		V _{DS} =20V, V _{GS} =0 T _J =55°C			5		
I _{D(ON)}	On=State Drain Current	V _{DS} ≧5V, V _{GS} =4.5V	7			Α	
	Drain-Source On-Resistance	V _{GS} =4.5V, I _D =7A	10	12.5	19	mΩ	
		V _{GS} =4.0V, I _D =7A	11	13.5	19		
RDS(ON)		V _{GS} =3.1V, I _D =6.5A	12	14	20		
		V _{GS} =2.5V, I _D =5.5A	13	16	22		
		V _{GS} =1.8V, I _D =5A	14	18	28		
G _{fs}	Forward Transconductance	V _{DS} =5V, I _D =7A		31		S	
Source-Dra	in Diode						
V _{SD}	Diode Forward Voltage	I _S =1.0A, V _{GS} =0V		0.7	1.3	V	
Dynamic Pa	arameters	•					
Qg	Total Gate Charge	V _{DS} =10V		16			
Q _{gs}	Gate-Source Charge	V _{GS} =4.5V		1.7		nC	
Q _{gd}	Gate-Drain Charge	I _D =7.0A		6.8			
Ciss	Input Capacitance	V _{DS} =10V		1120		pF	
Coss	Output Capacitance	V _{GS} =0V		1950			
Crss	Reverse Transfer Capacitance	f=1MHz		155			
T _{d(on)}	Turn-On Time	V _{DS} =10V		7.2			
Tr	ium-On time	I _D =7.0A		11			
T _{d(off)}	Tum Off Time	V _{GEN} =5V		64		nS	
Tf	Tum-Off Time	R _G =3.3Ω		32		1	

Note: 1. Pulse test: pulse width<=300uS, duty cycle<=2%

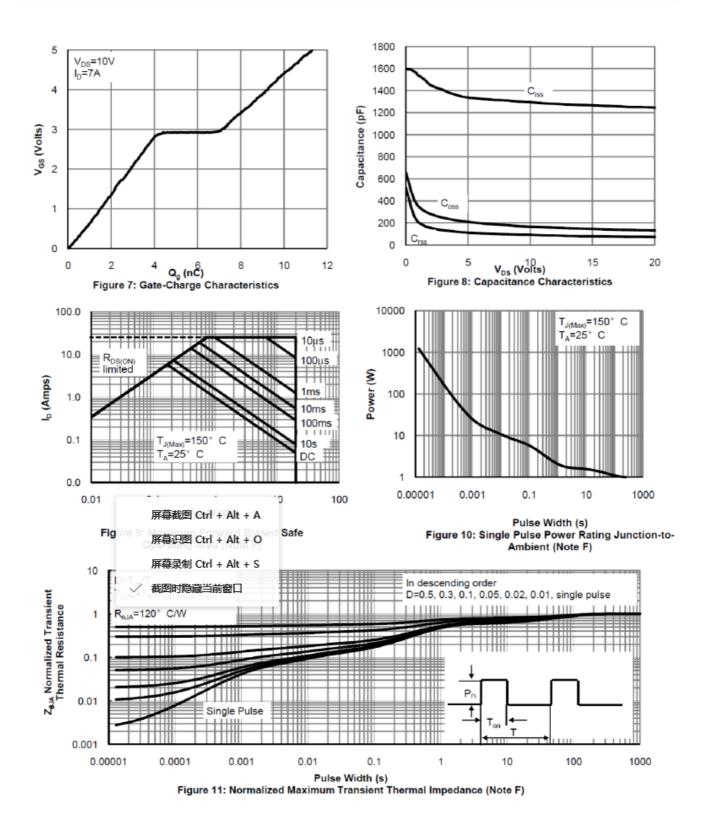
2.Static parameters are based on package level with recommended wire bonding



Typical Characteristics

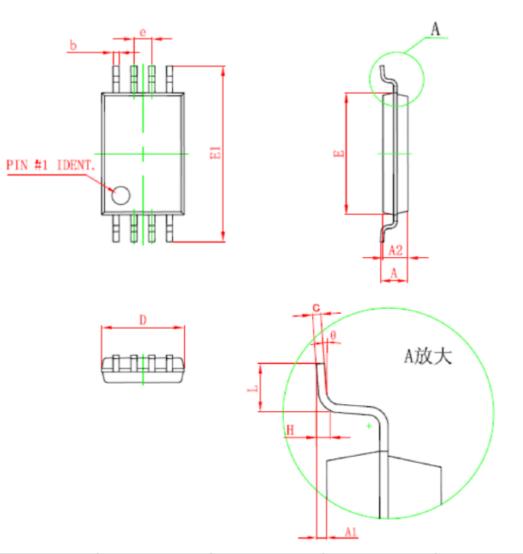








Package Mechanical Data-TSSOP-8



Symbol Symbol	Dimensions In Millimeters		Dimensions In Inches		
ЗУШООТ	Min	Max	Min	Max	
D	2.900	3.100	0.114	0.122	
E	4.300	4.500	0.169	0.177	
b	0.190	0.300	0.007	0.012	
с	0.090	0.200	0.004	0.008	
E1	6.250	6.550	0.246	0.258	
A		1.100		0.043	
A2	0.800	1.000	0.031	0.039	
A1	0.020	0.150	0.001	0.006	
e	0.65 (BSC)		0.026 (BSC)		
L	0.500	0.700	0.020	0.028	
H	0. 25 (TYP) 0. 01 (TYP)		(TYP)		
θ	1°	7°	1°	7°	